

FIG. 1 (PRIOR ART)

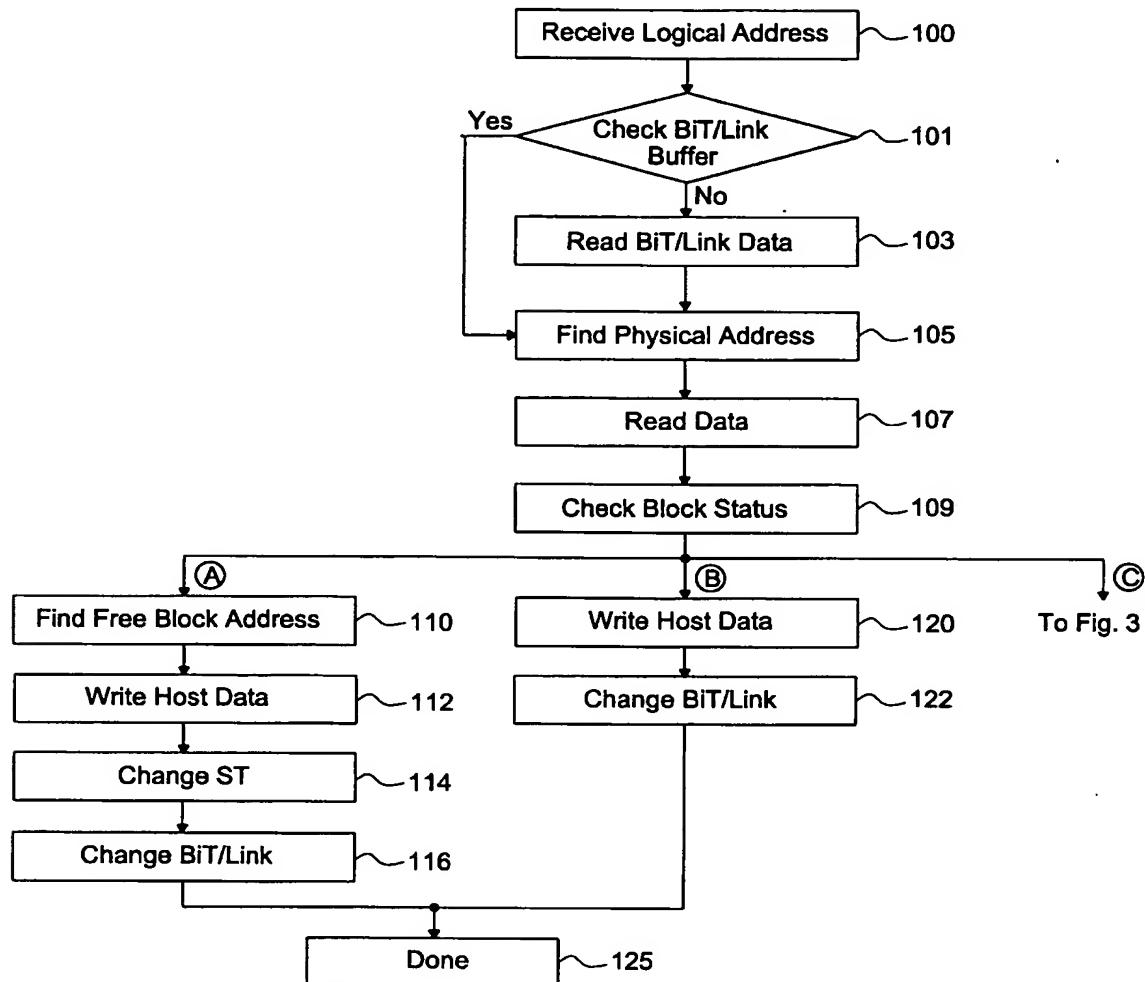


FIG. 2

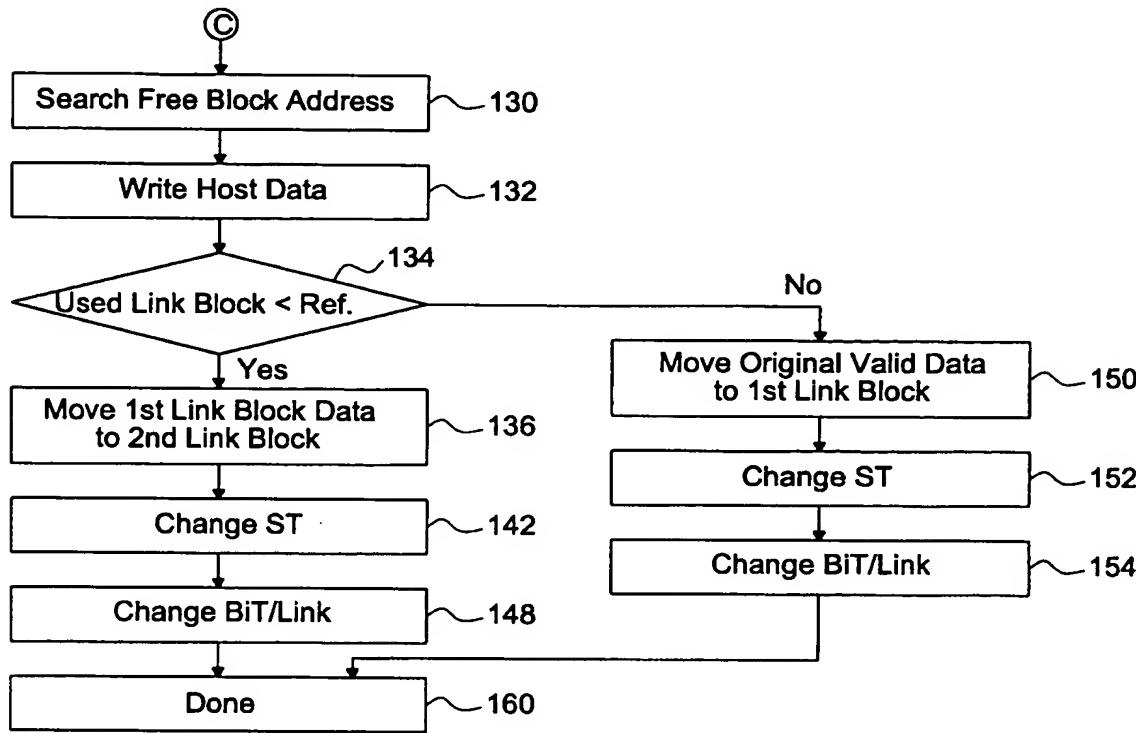


FIG. 3

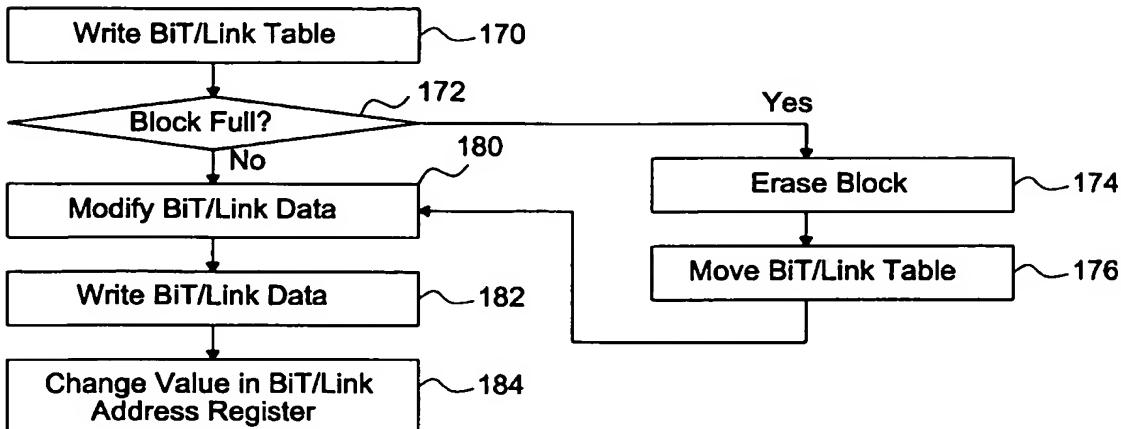


FIG. 4

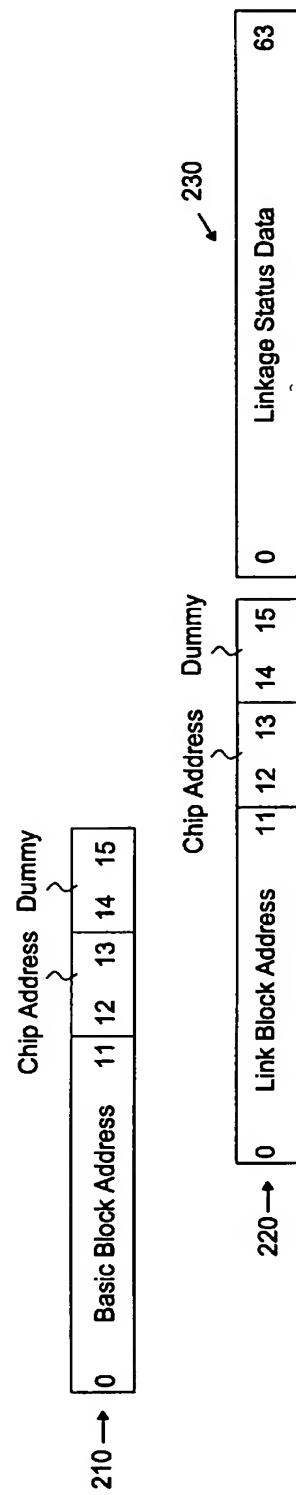


FIG. 5

Sang Wook HAN; Hee Seong KIM
APPARATUS AND METHOD FOR CONTROLLING FLASH MEMORIES
Attorney Docket No. 4820-3/Application No. Not Yet Assigned

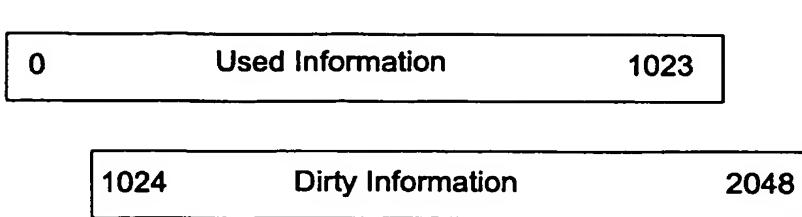


FIG. 6

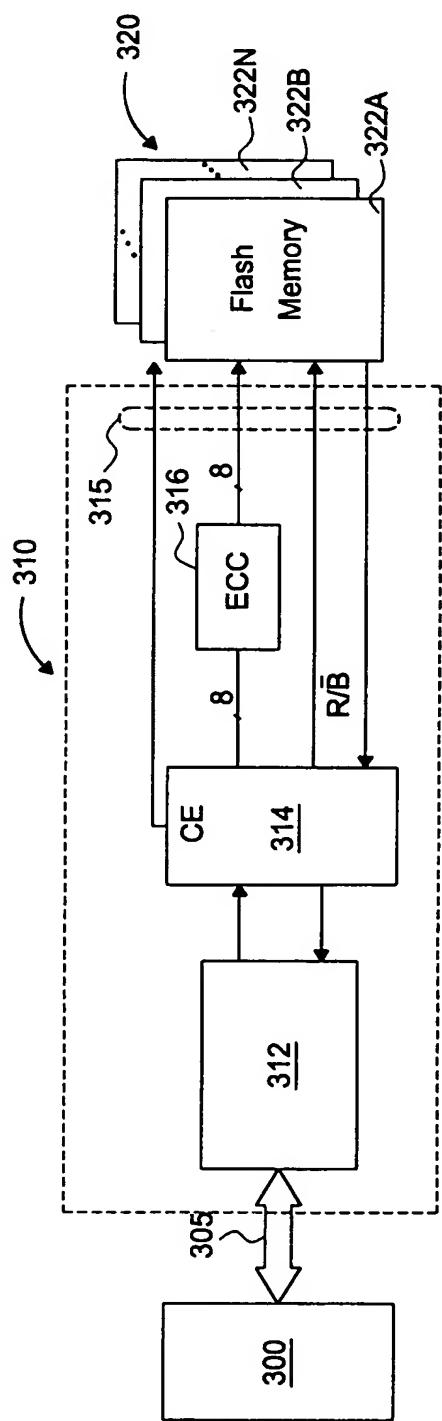


FIG. 7

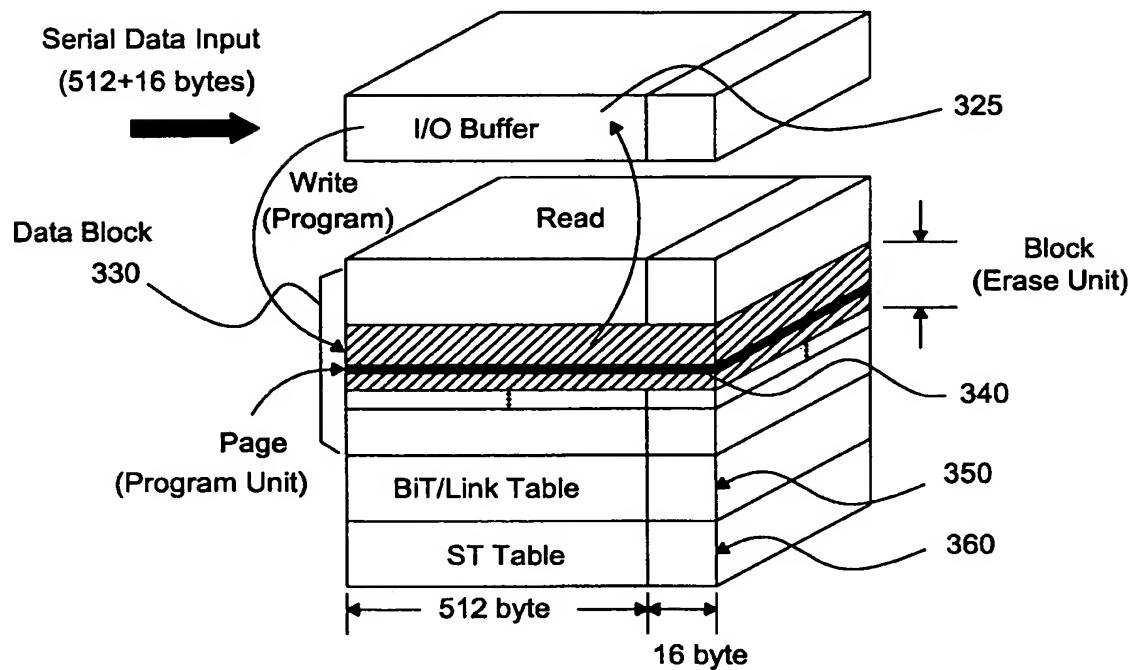
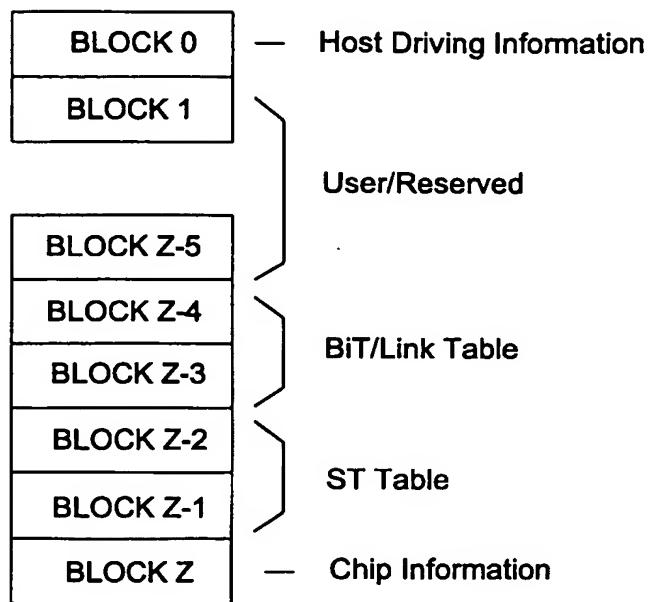


FIG. 8



350

FIG. 9

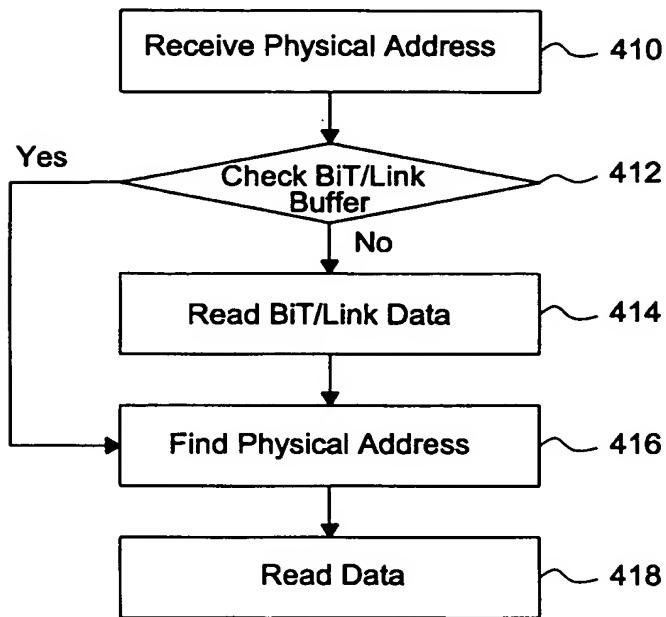


FIG. 10

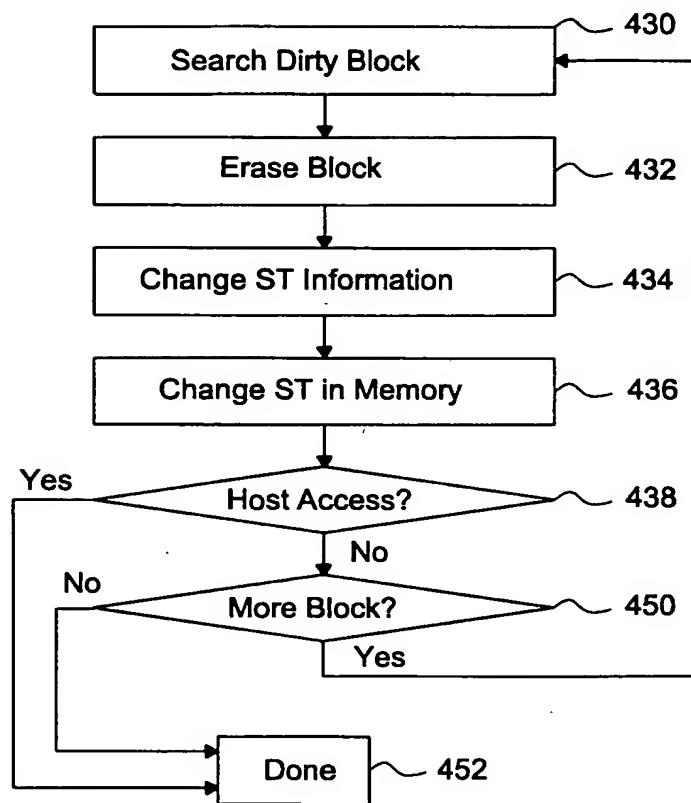


FIG. 11

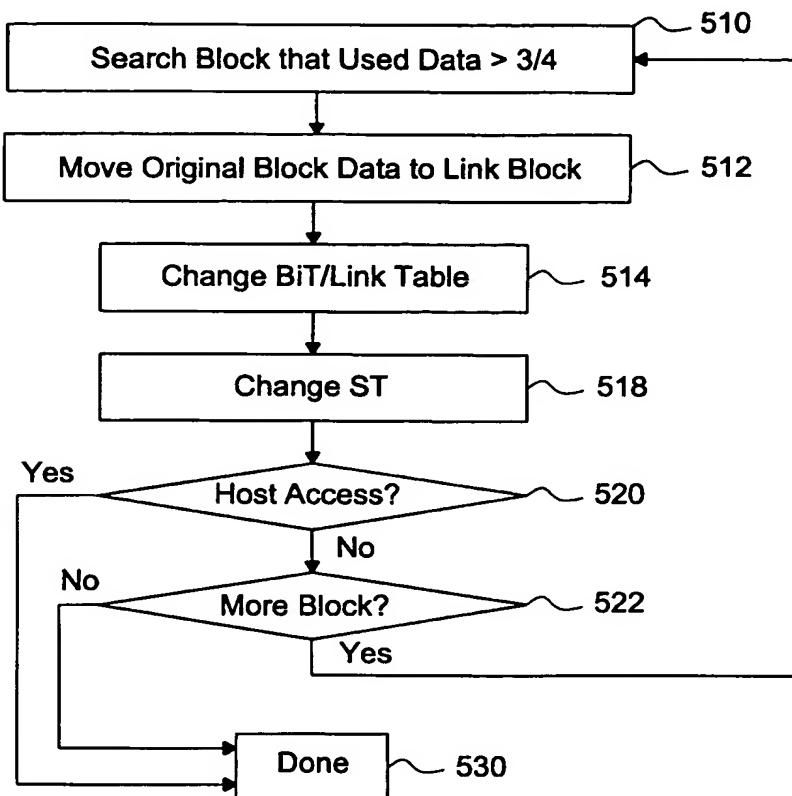


FIG. 12

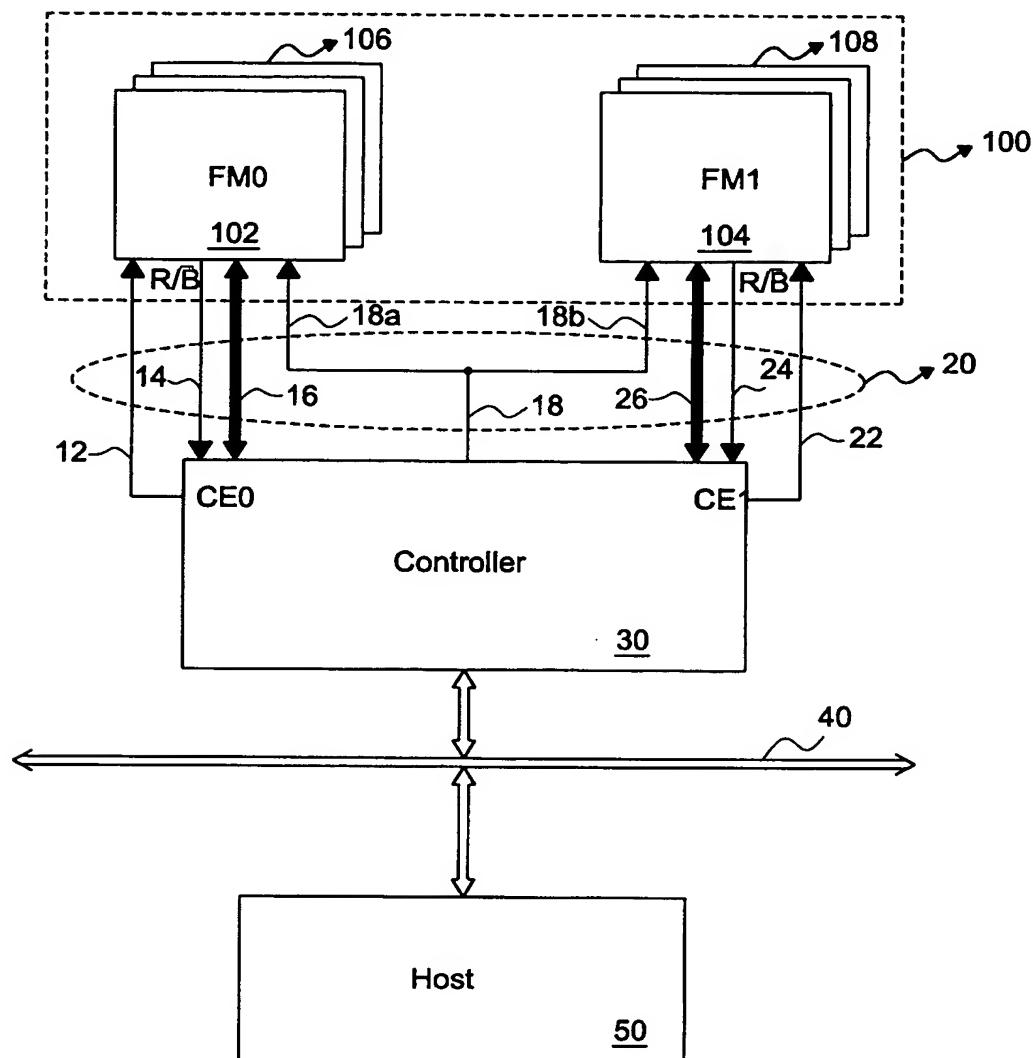


FIG. 13

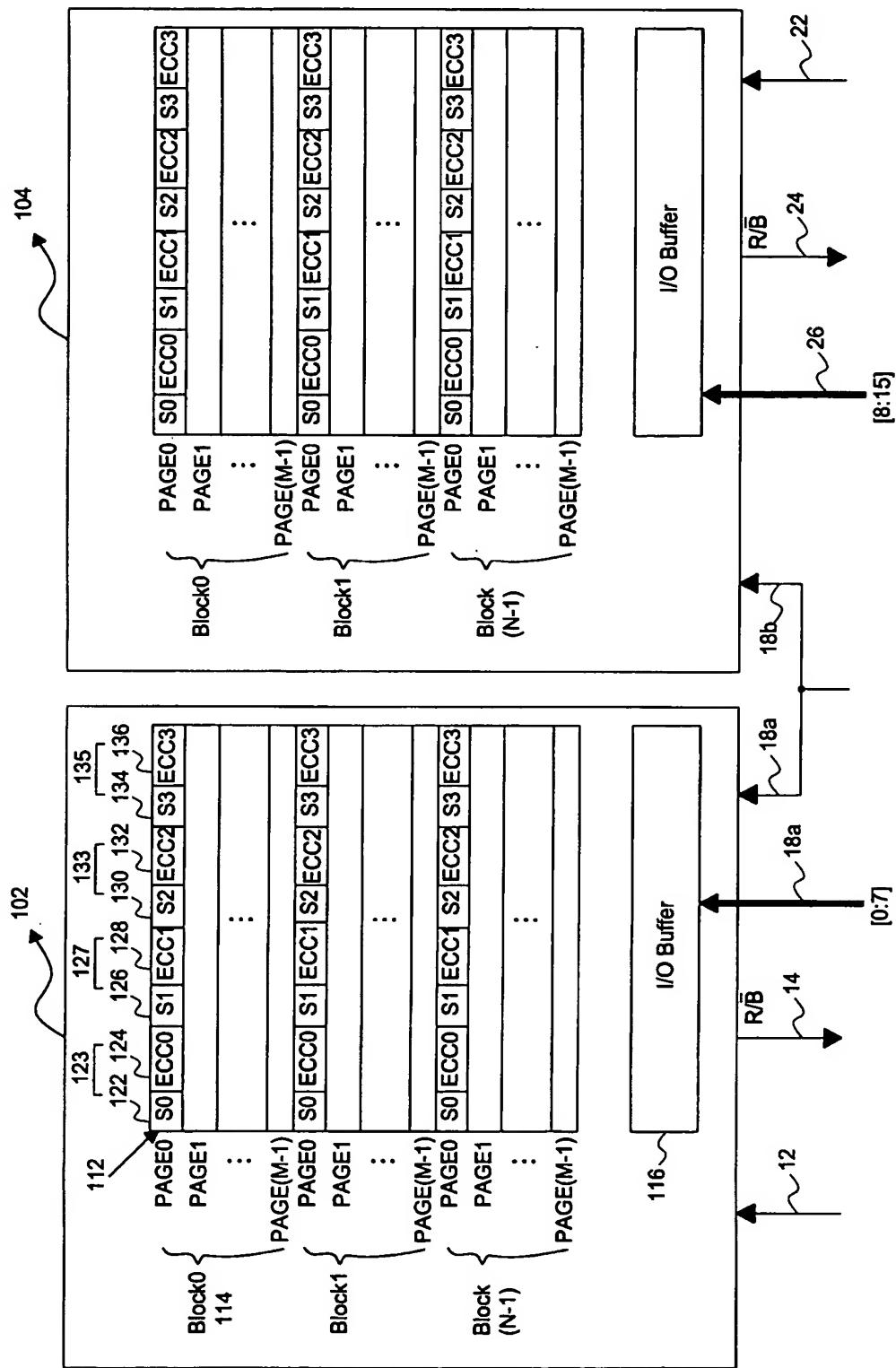


FIG. 14

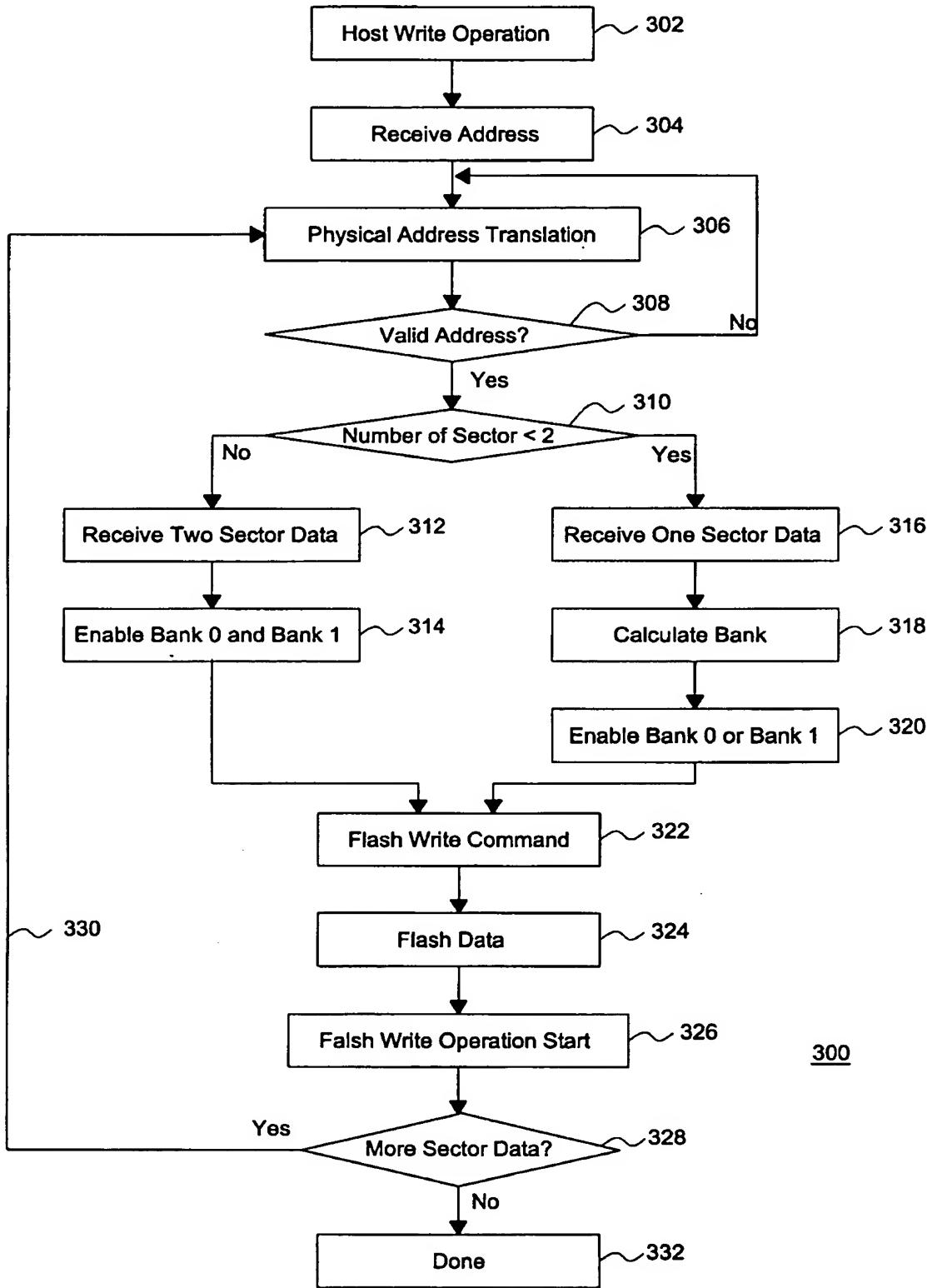


FIG. 15